

DC/DC regulators
Input 3.3V, 5V, 12V
Output 12A, 15A

Key Features

- 0.55 - 1.8 Vdc Output Voltage
- V_{TT} Bus Termination Output
- Tracks V_{ref} of DDR & QDR memories
- Inhibit On/Off, Over Current Protection
& Under-Voltage Lockout



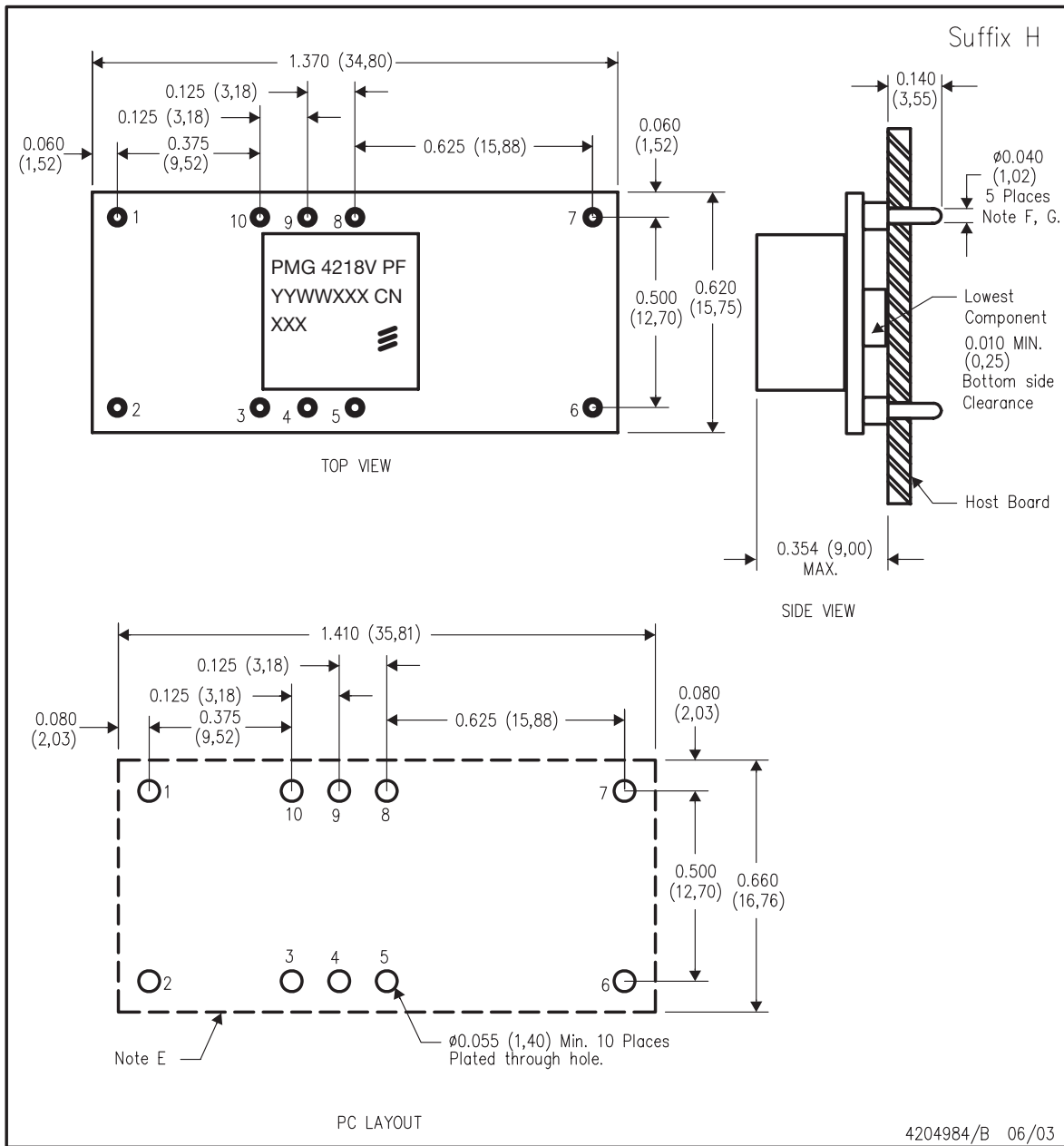
POLA™

The PMG F-series of DC/DC regulators (POL) are a ready-to-use switching regulator modules designed specifically for bus terminations in DDR and QDR memory applications. The high efficiency and high reliability of the PMG series makes them particularly suited for the communications equipment of today and tomorrow.

These products are manufactured using the most advanced technologies and materials to comply with environmental requirements. Designed to meet high reliability requirements of systems manufacturers, the PMG responds to world-class specifications.

Ericsson Power Modules is an ISO 9001/14001 certified supplier.

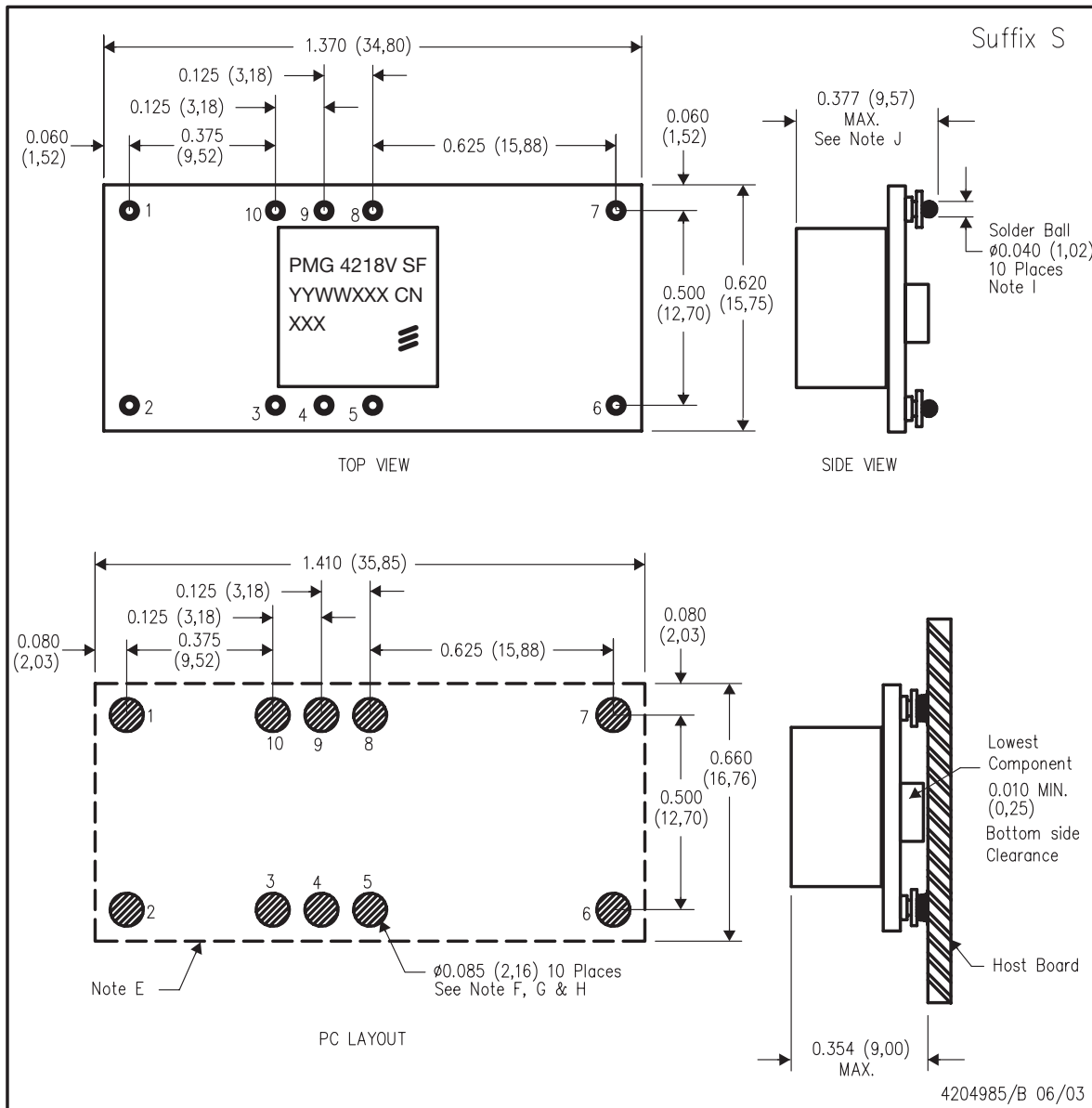
Mechanical Information - through-hole



- NOTES:
- All linear dimensions are in inches (mm).
 - This drawing is subject to change without notice.
 - 2 place decimals are ± 0.030 ($\pm 0,76$ mm).
 - 3 place decimals are ± 0.010 ($\pm 0,25$ mm).
 - Recommended keep out area for user components.

- Pins are 0.040" (1,02) diameter with 0.070" (1,78) diameter standoff shoulder.
- All pins: Material - Copper Alloy
Finish - Tin (100%) over Nickel plate

Mechanical Information - surface mount



- NOTES:
- A. All linear dimensions are in inches (mm).
 - B. This drawing is subject to change without notice.
 - C. 2 place decimals are ± 0.030 ($\pm 0,76$ mm).
 - D. 3 place decimals are ± 0.010 ($\pm 0,25$ mm).
 - E. Recommended keep out area for user components.
 - F. Power pin connection should utilize two or more vias to the interior power plane of 0.025 (0,63) I.D. per input, ground and output pin (or the electrical equivalent).

- G. Paste screen opening: 0.080 (2,03) to 0.085 (2,16).
Paste screen thickness: 0.006 (0,15).
- H. Pad type: Solder mask defined.
- I. All pins: Material – Copper Alloy
Finish – Tin (100%) over Nickel plate
Solder Ball – See product data sheet.
- J. Dimension prior to reflow solder.

Product Program

Vin [V]	Vout [V]	Iout [A]	Pin Configuration	Part No.	Comment
2.95 - 3.65	0.55-1.8	15	TH	PMG 4218V PF	Samples Available
2.95 - 3.65	0.55-1.8	15	SMD	PMG 4218V SF*	Samples Available
4.5 - 5.5	0.55-1.8	15	TH	PMG 5218V PF	Samples Available
4.5 - 5.5	0.55-1.8	15	SMD	PMG 5218V SF*	Samples Available
10.8 - 13.2	0.55-1.8	12	TH	PMG 8218V PF	Samples Available
10.8 - 13.2	0.55-1.8	12	SMD	PMG 8218V SF*	Samples Available

* Available as Leadfree and RoHS compliant versions. Add suffix "R".

Pin configuration

Pin	Function
1	GND
2	V _{IN}
3	Inhibit*
4	No Connect
5	V _O sense
6	V _{TT}
7	GND
8	V _{ref}
9	No Connect
10	No Connect

* Denotes negative logic:
Open = Normal operation
Ground = V_{TT} output off

New Product Overview is intended for discussion to determine interest and requirements. Information provided is believed to be accurate and reliable. Ericsson Power Modules makes no commitment to sample or manufacture for sale these products and reserves the right to make changes to the product, or information contained herein, without notice.

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[New Product Overview](#)

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